

### Features

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

### Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

$I_{T(AV)}$             **920A**  
 $V_{DRM}/V_{RRM}$     **400~1000V**  
 $I_{TSM}$                 **11 kA**  
 $I^2t$                     **605 10<sup>3</sup>A<sup>2</sup>S**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T <sub>J</sub> (°C)	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T <sub>C</sub> =70°C				920	
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms		125	400		1000	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$		125			40	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave		125			11.0	kA
$I^2t$	$I^2t$ for fusing coordination	$V_R=0.6V_{RRM}$					605	A <sup>2</sup> s*10 <sup>3</sup>
$V_{TO}$	Threshold voltage			125			0.80	V
$r_T$	On-state slope resistance						0.40	mΩ
$V_{TM}$	Peak on-state voltage	$I_{TM}=1500A, F=15kN$		25			1.80	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$		125			1000	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 1300A, Gate pulse t <sub>r</sub> ≤0.5μs I <sub>GM</sub> =1.5A		125			100	A/μs
Q <sub>rr</sub>	Recovery charge	$I_{TM}=1000A, tp=2000μs, di/dt=-20A/μs,$ $V_R=50V$		125		1100		μC
$I_{GT}$	Gate trigger current			25	35		250	mA
$V_{GT}$	Gate trigger voltage	$V_A=12V, I_A=1A$			0.8		2.5	V
$I_H$	Holding current				20		200	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$		125	0.3			V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 15kN					0.035	°C /W
$R_{th(c-h)}$	Thermal resistance case to heat sink						0.008	
$F_m$	Mounting force				10		20	kN
T <sub>stg</sub>	Stored temperature				-40		140	°C
W <sub>t</sub>	Weight					240		g
Outline	KT33cT							

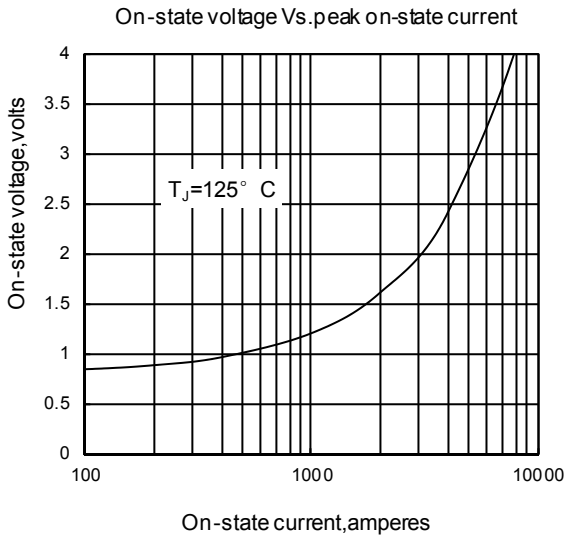


Fig1

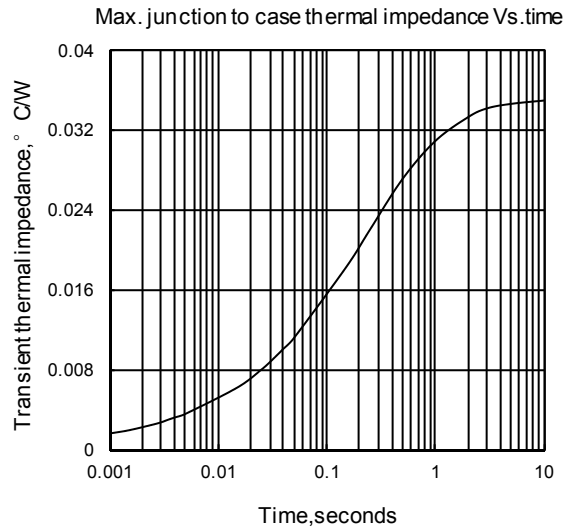


Fig2

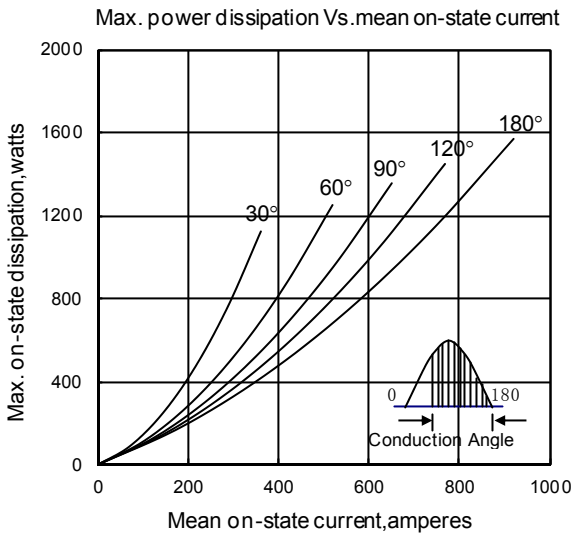


Fig3

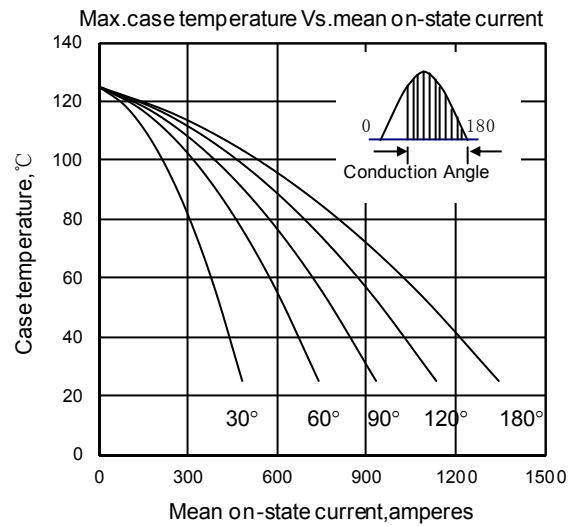


Fig4

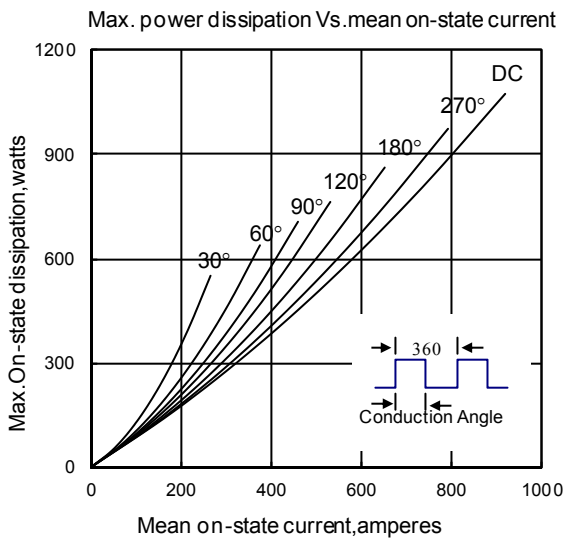


Fig5

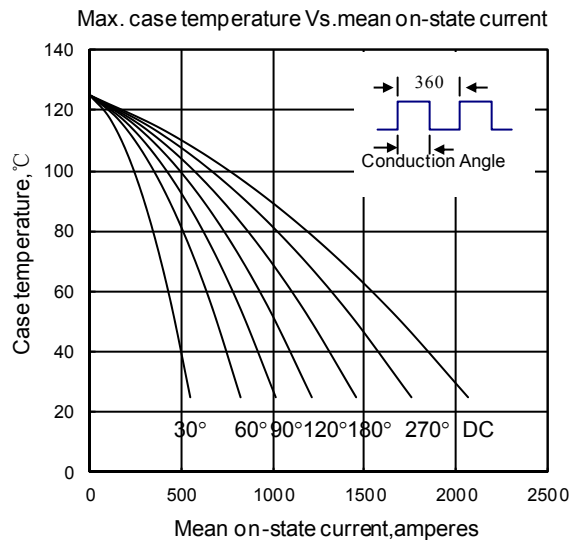


Fig6

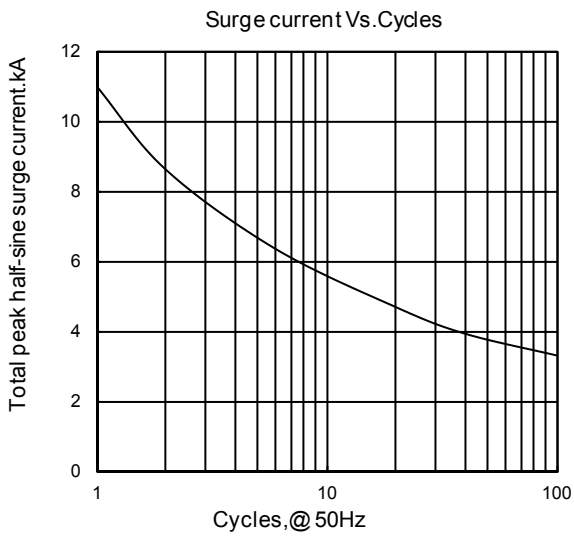


Fig7

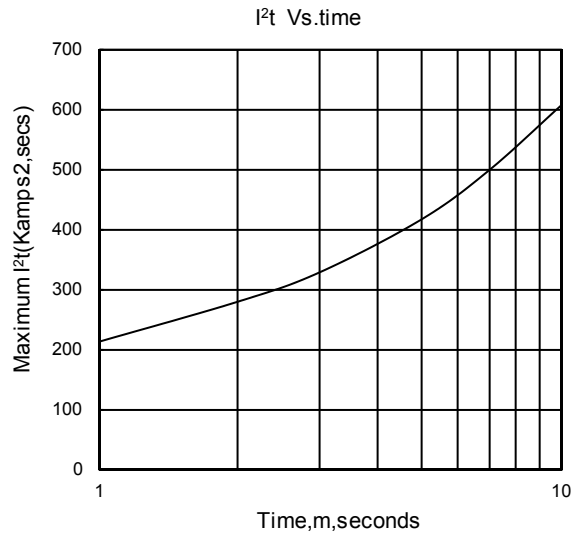


Fig8

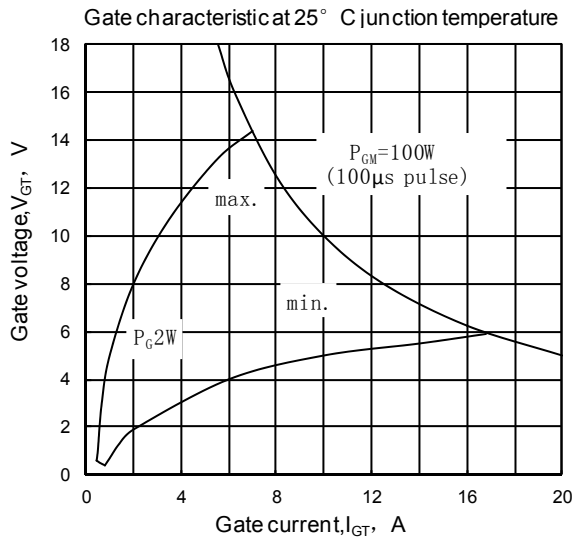


Fig9

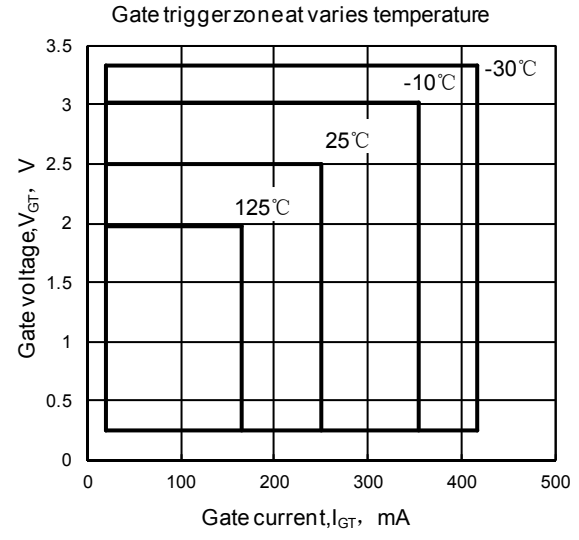


Fig10

Outline:

